



Ref. No: SVEC/ECE/Circular/2023-24/87

CLASS CONSOLIDATED TIME TABLE

Class: IV Semester

Section: A

Class Coordinator: **Sri D. R. Sandeep**

w. e. f. 05-02-2024

Room No.: D-302

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CS	ADC	MEFA	DICA	Lunch Break	CS	ADC	LIB
Tue	DICA	Python Lab				Python	EMTL	MEFA
Wed	PCS-II(A)		MEFA	EMTL		ADC	DICA	CS
Thu	EMTL	PCS-II(E)		Python		ADC Lab		
Fri	Python	EMTL	CS	ADC		DICA Lab		
Sat	ADC	DICA	EMTL	CS		DICA	MEFA	Sports

Section: B

Class Coordinator: **Sri P.V.V. Satyanarayana**

Room No.: D-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	DICA	EMTL	CS	ADC	Lunch Break	Python Lab		
Tue	CS	DICA	ADC	MEFA		ADC	CS	EMTL
Wed	ADC	EMTL	PCS-II(A)			CS	DICA	LIB
Thu	EMTL	DICA Lab				DICA	Python	MEFA
Fri	CS	Python	PCS-II(E)			MEFA	ADC	Sports
Sat	MEFA	Python	DICA	EMTL		ADC Lab		

Section: C

Class Coordinator: **Sri Thota Sreenivas**

Room No.: D-304

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CS	ADC Lab			Lunch Break	EMTL	DICA	MEFA
Tue	EMTL	ADC	DICA	MEFA		Python	CS	LIB
Wed	Python	CS	EMTL	ADC		Python Lab		
Thu	ADC	MEFA	PCS-II(E)			DICA	PCS-II(A)	
Fri	CS	DICA Lab				ADC	EMTL	Python
Sat	DICA	EMTL	ADC	DICA		CS	MEFA	Sports

Staff Details:

S. No.	Sub Code	COURSE NAME	SECTION		
			A	B	C
1.	V20ECT07	Analog & Digital Communication	Sri D. VENKANNA BABU	Sri D. VENKANNA BABU	Sri J. RAJENDRA
2.	V20ECT08	Digital IC Application	Sri D.R.SANDEEP	Dr. B. ASHOK KUMAR	Sri P.GOPALA REDDY
3.	V20ECT09	Electromagnetic Wave & Transmission Lines	Sri G. SHANKARA BHASKARA RAO	Sri P.V.V. SATYANARAYANA	Sri THOTA SREENIVAS
4.	V20EET11	Control Systems	Sri P.NAGARAJU	Sri P.NAGARAJU	Dr. S. KAMESH
5.	V20MBT51	Managerial Economics & Financial Analysis	Smt. V.SANDHYA	Smt. V.SANDHYA	Dr. K. RAMBABU
6.	V20CSL33	Python Programming Lab	Dr. T.D.N.S.S. SARVESWARARAO Sri. B. MURALI KRISHANA	Sri. B. MURALI KRISHANA Dr. T.D.N.S.S. SARVESWARARAO	Sri. B. MURALI KRISHANA Dr. M. THAMARAI
7.	V20ECL04	Analog & Digital Communication Lab	Sri D. VENKANNA BABU Sri G. SHANKARA BHASKARA RAO Sri P.NAGARAJU Sri R. RAM PRASAD	Sri J. RAJENDRA Sri D. VENKANNA BABU Dr. T.V.N.L. ASWINI Sri P.V.V. SATYANARAYANA	Sri J. RAJENDRA Sri G. SHANKARA BHASKARA RAO Dr. T.V.N.L. ASWINI Sri R. RAM PRASAD
8.	V20ECL05	Digital IC Application Lab	Sri D.R.SANDEEP Dr. B. ASHOK KUMAR Sri P.NAGARAJU	Dr. B. ASHOK KUMAR Smt. Y.SUJATHA Sri D.R.SANDEEP	Sri P.GOPALA REDDY Dr. B. ASHOK KUMAR Sri D.R.SANDEEP
9.	V20ENT03	Professional Communication Skills-II	Sri THS SRINIVAS (A) U. APARANJANI (E) Sri M.VENKATA RAMANA (E)	Sri JNV SOMAYAJULU (A) CH. TANUJA (E) Sri M.VENKATA RAMANA (E)	Sri THS SRINIVAS (A) Ms A. KIRANMAYI (E) Dr. B.ANANDA RAO (E)

E. Kumaran

Head of the Department

Vision: To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



SRI VASAVI ENGINEERING COLLEGE
Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.
Department Of Electronics and Communication Technology



Ref. No: SVEC/ECE/Circular/2023-24/87

CLASS CONSOLIDATED TIME TABLE

Class: IV Semester

w. e. f. 05-02-2024

Section: ECT

Class Coordinator: **Sri R. Ram Prasad**

Room No.: D-305

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	EMTL	DICA	CS	MEFA	Lunch Break	ADC	DICA	Sports
Tue	DICA	CS	EMTL	ADC		DICA LAB		
Wed	ADC	Python	MEFA	CS		DICA	PCS-II(A)	
Thu	MEFA	ADC LAB				PCS-II(E)		CS
Fri	EMTL	Python Lab				ADC	Python	LIB
Sat	Python	ADC	DICA	EMTL		CS	EMTL	MEFA

Staff Details:

S. NO.	SUB CODE	COURSE NAME	SECTION
			ECT
1.	V20ECT07	Analog & Digital Communication	Dr. T.V.N.L. ASWINI
2.	V20ECT08	Digital IC Application	Smt. Y. SUJATHA
3.	V20ECT09	Electromagnetic Wave & Transmission Line	Sri R.RAM PRASAD
4.	V20EET11	Control Systems	Dr. PURNIMA K SHARMA
5.	V20MBT51	Managerial Economics & Financial Analysis	Sri R. V. RAJA SEKHAR
6.	V20CSL33	Python Programming Lab	Dr. M. THAMARAI Dr. T. D. N. S. S. SARVESWARA RAO
7.	V20ECL04	Analog & Digital Communication Lab	Dr. T.V.N.L. ASWINI Sri R.RAM PRASAD Sri J.RAJENDRA Sri P.NAGARAJU
8.	V20ECL05	Digital IC Application Lab	Smt. Y. SUJATHA Sri P.GOPALA REDDY
9.	V20ENT03	Professional Communication Skills-II	Sri THS SRINIVAS (A) Ms A. KIRANMAYI (E) Dr. K. VENKATA RAO (E)

E. Kumaresh

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SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-584 101, W.G. Dist.

Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2023-24/77

CLASS CONSOLIDATED TIME TABLE

Class: VI Semester

Section: A

Class Coordinator:

w. e. f. 28-12-2023

Room No.:

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SDTV	DSP	MWE	LIB	Lunch Break	MWE Lab		
Tue	MS	IoT	MWE	SDTV		DSP	IoT	Sports
Wed	DSP	IoT Lab				MS	SDTV	IoT
Thu	IoT	MWE	SDTV	DSP		DSP Lab		
Fri	MS	PEH	IoT	DSP		MWE	DSP	SDTV
Sat	SDTV	IoT	MWE	MS		PEH	MWE	MS

Section: B

Class Coordinator:

Room No.:

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	MS	MWE	SDTV	DSP	Lunch Break	PEH	IoT	DSP
Tue	MWE	SDTV	DSP	IoT		MWE Lab		
Wed	SDTV	PEH	MWE	SDTV		IoT	DSP	MS
Thu	IoT	DSP	MS	MWE		IoT Lab		
Fri	DSP	IoT	MS	SDTV		MWE	IoT	LIB
Sat	MWE	DSP Lab				MS	SDTV	Sports

Section: C

Class Coordinator:

Room No.:

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	IoT	IoT Lab			Lunch Break	DSP	SDTV	MS
Tue	MWE	IoT	DSP	SDTV		PEH	IoT	Sports
Wed	MS	DSP	SDTV	MWE		MWE Lab		
Thu	DSP	MWE	IoT	PEH		SDTV	MS	LIB
Fri	MWE	SDTV	DSP	MS		DSP Lab		
Sat	SDTV	IoT	MWE	MS		MWE	IoT	DSP

Staff Details:

S. No.	Sub	COURSENAME	SECTION		
			A	B	C
1.	DSP	Digital Signal Processing	Dr. M.KOTESWARA RAO	Dr. M.KOTESWARA RAO	Sri T.SREENIVASU
2.	MWE	Microwave Engineering	Sri M. VINOD KUMAR	Sri K. PASIPALANA RAO	Sri G.V.SUBRAHMANYAM
3.	IOT	Internet of things	Sri KNH SRINIVAS	Sri R.L.R LOKESH BABU	Sri R.L.R LOKESH BABU
4.	SDTV	System Design Through VERILOG	Sri M. PITCHAIAH	Sri M. PITCHAIAH	Dr. S.V.V. SATYANARAYANA
5	PEH	Professional ethics and human Values	Dr. E.KUSUMA KUMARI	Dr. E.KUSUMA KUMARI	Dr. M. THAMARAI
6	MS	Management Science	Sri K. PAVAN KUMAR	Dr. K. PULLA RAO	Sri P. BHARATH KUMAR
7	DSP LAB	Digital Signal Processing Lab	Dr. M.KOTESWARA RAO Sri S. KAMESH Sri M. PITCHAIAH	Dr. M.KOTESWARA RAO Sri KNH SRINIVAS Sri M. PITCHAIAH	Sri T.SREENIVASU Sri S. KAMESH Dr. PURNIMA K SHARMA
8	MWE LAB	Microwave Engineering Lab	Sri M. VINOD KUMAR Sri K. PASIPALANA RAO Sri PVV SATYANARAYANA	Sri K. PASIPALANA RAO Sri M. VINOD KUMAR Sri THOTA SREENIVAS	Sri THOTA SREENIVAS Sri G. SHANKARA BHASKARA RAO Sri M. VINOD KUMAR
9	IOT LAB	Internet of things Lab	Sri KNH SRINIVAS Sri R.L.R LOKESH BABU Sri PVV RAJESH	Sri R.L.R LOKESH BABU Sri KNH SRINIVAS Sri V. ANIL KUMAR M.Tech Student	Sri R.L.R LOKESH BABU Sri V. ANIL KUMAR Sri KNH SRINIVAS M.Tech Student

E. K. K. K.

Head of the Department

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Department Of Electronics and Communication Technology



CLASS CONSOLIDATED TIME TABLE

Class: VI Semester

w. e. f. 28-12-2023

Section: ECT

Class Coordinator:

Room No.:

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SDTV	MWE	DSP	MS	Lunch Break	PEH	IoT	DSP
Tue	IoT	SDTV	MS	MWE		DSP Lab		
Wed	MWE	PEH	IoT	DSP		SDTV	MS	MWE
Thu	MS	IoT	SDTV	IoT		DSP	MWE	Sports
Fri	DSP	MWE Lab				SDTV	MWE	LIB
Sat	IoT	DSP	MS	SDTV		IoT lab		

Staff Details:

S. No.	COURSE	COURSENAME	Section
			ECT
1.	DSP	Digital Signal Processing	Sri T.SREENIVASU
2.	MWE	Microwave Engineering	Sri G.V.SUBRAHMANYAM
3.	IOT	Internet of things	Sri V. ANIL KUMAR
4.	SDTV	System Design Through VERILOG	Dr. S.V.V. SATYANARAYANA
5	PEH	Professional ethics and human Values	Dr. PURNIMA K SHARMA
6	MS	Management Science	Smt. K. LALITHA BHAVANI
7	DSP LAB	Digital Signal Processing Lab	Sri M. PITCHAI AH Sri T.SREENIVASU Dr. PURNIMA K SHARMA
8	MWE LAB	Microwave Engineering Lab	Sri THOTA SREENIVAS Sri G.V.SUBRAHMANYAM Sri PVV SATYANARAYANA
9	IOT LAB	Internet of things Lab	Sri V. ANIL KUMAR Sri PVV RAJESH M.Tech Student

E. Kumarakani

Head of the Department

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